J K FLIP FLOP TRAINER

MODEL - DFFO4

This trainer has been designed with a view to provide practical and experimental knowledge of different types of Flip Flops.



SPECIFICATIONS

- 1. Power supply requirement : 230V AC, 50 Hz.
- 2. Built in IC based regulated Power supply 0-5 V DC/200 mA.
- 3. Following parts provided on Single PCB with connecting terminals.
 - 1. JK FLIP FLOP IC 7473 1 No.
 - High /Low switches : 6 Nos provided to apply 0 and 1 level
 - 3. Logic output Indicators : 4 LEDs provided for output level indication.
 - 4. Pulsar Switches : 1 Nos provided.
- 4. All parts are soldered on single PCB of size 8" x 7" with complete circuit diagram screen-printed in multicolour.
- 5. Standard Accessories :

2.

SIGMA

- 1. User Manual with practical and circuit diagrams.
- 2. Connecting patch cords 5 nos.

In keeping view of SIGMA policy of continuous development and improvement, the Specifications may be changed without prior notice or obligation.

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